

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1207	trench same (floating gate)	USPAT	ADJ	ON	2006/01/04 14:18
L2	181	L1 same stacks	USPAT	ADJ	ON	2006/01/04 14:19
L3	12	L1 same (shield or shielding)	USPAT	ADJ	ON	2006/01/04 14:49
L4	74	L1 and (shield or shielding)	USPAT	ADJ	ON	2006/01/04 14:52
L5	898	L1 same memory	USPAT	ADJ	ON	2006/01/04 14:55
L6	1727	(257/314-315).CCLS.	USPAT	OR	OFF	2006/01/04 14:55
L7	281	L5 and L6	USPAT	ADJ	ON	2006/01/04 14:56
L8	2801	(257/314-320).CCLS.	USPAT	OR	OFF	2006/01/04 15:12
L9	855	trench same (shield or shielding)	USPAT	ADJ	ON	2006/01/04 15:12
L10	18	L9 and L8	USPAT	ADJ	ON	2006/01/04 15:12
L11	1693	trench same (floating gate)	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/04 15:14
L12	9	L11 same (shield or shielding)	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/04 15:14
L13	143	L11 same stacks	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/01/04 15:15
L14	398	L5 and L8	USPAT	ADJ	ON	2006/01/04 15:18
L15	1634	vertical transistor	USPAT	ADJ	ON	2006/01/04 15:20
L16	58	L1 and L15	USPAT	ADJ	ON	2006/01/04 15:20